

Amendments to the Claims:

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-13 (canceled).

Claim 14 (new): An optical receiver circuit, comprising:

a differential amplifier including a first input and a second input;

an optical reception device connected to said first input of said differential amplifier, said optical reception device having an electrical behavior in an illumination-free case; and

an electrical element for simulating the electrical behavior of said optical reception device in the illumination-free case, said electrical element connected to said second input of said differential amplifier.

Claim 15 (new). The optical receiver circuit according to claim 14, further comprising:

a first preamplifier connecting said optical reception device to said differential amplifier; and

a second preamplifier connecting said electrical element to said differential amplifier.

Claim 16 (new). The optical receiver circuit according to claim 14, wherein:

said electrical element is formed by a darkened, further reception device.

Claim 17 (new). The optical receiver circuit according to claim 16, wherein:

said optical reception device and said further reception device are monolithically integrated on a chip.

Claim 18 (new). The optical receiver circuit according to claim 14, further comprising:

a first preamplifier connecting said optical reception device to said differential amplifier; and

a second preamplifier connecting said electrical element to said differential amplifier, said first preamplifier and said second preamplifier being identical.

Claim 19 (new). The optical receiver circuit according to claim 18, wherein:

said first preamplifier is a transimpedance amplifier; and

said second preamplifier is a transimpedance amplifier.

Claim 20 (new). The optical receiver circuit according to claim 18, further comprising:

an integrated control circuit;

said first preamplifier being a transimpedance amplifier having a feedback impedance with a magnitude being settable by a user via said integrated control circuit; and

said second preamplifier being a transimpedance amplifier having a feedback impedance with a magnitude being settable by a user via said integrated control circuit.

Claim 21 (new). The optical receiver circuit according to claim 20, wherein:

said integrated control circuit is connected symmetrically to said feedback impedance of said first preamplifier and to said feedback impedance of said second preamplifier.

Claim 22 (new). The optical receiver circuit according to claim 14, wherein:

said optical reception device and said electrical element are connected to a common supply voltage.

Claim 23 (new). The optical receiver circuit according to claim 22, further comprising:

a low-pass filter connected to the common supply voltage.

Claim 24 (new). The optical receiver circuit according to claim 14, wherein:

said optical reception device is a photodiode; and

said electrical element is a photodiode.

Claim 25 (new). The receiver circuit according to claim 14, further comprising:

a package for packaging said differential amplifier, said optical reception device, and said electrical element, said package being selected from the group consisting of a TO-46 package, a TSSOP10 package, and a VQFN20 package.

Claim 26 (new). The receiver circuit according to claim 25, further comprising:

an integrated control circuit having a control terminal, said package having a terminal pin forming said control terminal.